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REVISIONS		
DESCRIPTION	DATE	APPROVED

D

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C

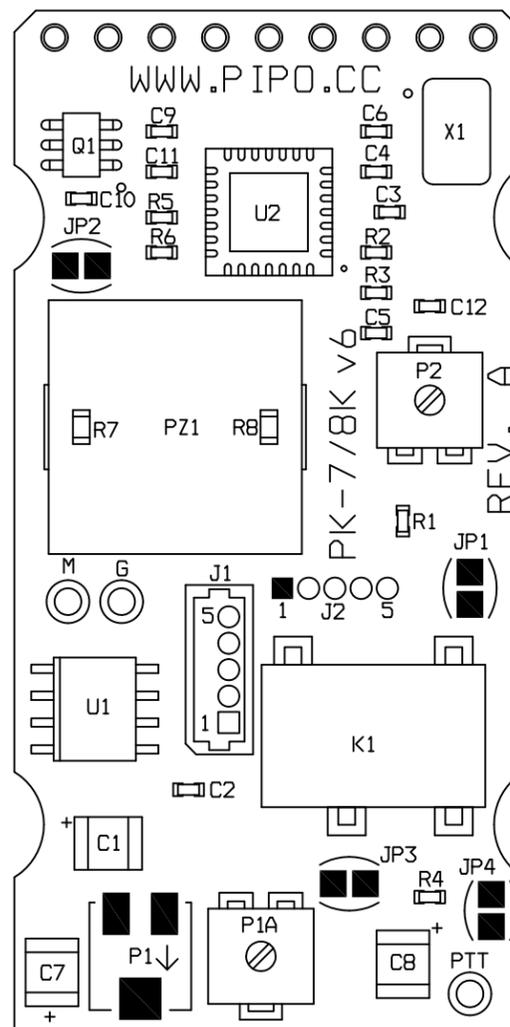
C

B

B

A

A



5. J2, 9 HOLES AT TOP OF BOARD, "PTT", "M" AND "G" HOLES ARE TO BE VOID OF SOLDER.
4. R7 AND R8 ARE USED AS SPACERS TO INSTALL PZ1. PZ1 IS INSTALLED BY HAND. IT IS NOT SEALED.
3. DO NOT INSTALL THE FOLLOWING COMPONENTS:  
J2, JP1-JP4.

2. INSTALL COMPONENTS, MAKE ALL INTERFACIAL CONNECTIONS, ASSEMBLE AND SOLDER IAW J-STD-001, CLASS 2.

1. SEE SEPARATE PARTS LIST FOR COMPONENT VALUE AND/OR USAGE.

**NOTES:** UNLESS OTHERWISE SPECIFIED.

UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE IN INCHES TOLERANCES ARE: <b>DECIMALS</b> .X = ± .1 .XX = ± .03 .XXX = ± .005  <b>ANGLES</b> ± 0°30'  DO NOT SCALE DRAWING	CONTRACT NO.		<b>PIPO COMMUNICATIONS</b> HOLLYWOOD, CALIFORNIA			
	APPROVALS	DATE				
	TREATMENT	DWN. R. GONZALES	6/13/2018	TITLE PCB ASSEMBLY: PK-7/8K v6		
	FINISH	CHK.				
THIS DRAWING PRODUCED BY: P.C.D.C. CAD FILE PK-7-8K v6-ASSY		APP.	SIZE	CAGE CODE	DWG NO.	REV
			B			A
		SCALE 3 : 1		SHEET 1 OF 1		

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